

SMART CARD READER CIRCUIT AND METHOD OF MONITORING

Abstract of the Disclosure

5 A smart card reader (8) includes a detection circuit (26)
that has a plurality of inputs (30, 38, 42) for monitoring a
plurality of operating conditions of the smart card reader. A
plurality of outputs (53-56) provide a plurality of sense
signals (VCCOK, VCCOC, VBATOK, CRDINS). A multiplexer (60)
10 has a plurality of sense inputs coupled to the plurality of
outputs of the detection circuit. A selection input (67, 68)
receives a selection signal (ADDR) for routing one of the
plurality of sense signals to an output (32) as a status
signal (STATUS).